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[HH490 - HH490 Release 1 : Default Detect Version]

Phase: DEVELOPMENT

Distribution: EXTERNAL

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Linux Kernel 4.4.94 : GNU General Public License v2.0 only

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Linux Kernel 4.4.94

long_tail:git.kernel.org/pub/scm/linux/kernel/git/stable/linux#v4.4.94

```
&copy)) {
    mpsslog("Tun write failed
    strerror(errno));
    mpsslog("len 0x%x ", len);
    mpsslog("read_len 0x%x\n",
    &copy)) {
        notifier = list_entry(copy.next, struct
rds_notifier, n_list);
    &copy)) {
        spin_lock_irqsave(
    &copy));
    &copy));
    &copy));
        disp_iovec(mic, copy, __func__,
        __LINE__);
    mpsslog("%s %s %d ", mic->name,
__func__, __LINE__);
    mpsslog("wrote to tap 0x%x\n",
    &copy));
    &copy));
    &copy));
    &copy));#endif
    /* Reinitialize IOV for next run
*/
    iov0[1].iov_len = MAX_NET_PKT_SIZE;
    } else if (len < 0) {
    &copy);
    if (
    /* Set the
correct output iov_len */
    iov1->iov_len =
copy.out_len;
    &copy);
    if (#ifdef DEBUG
    struct virtio_net_hdr *hdr
    = (struct
virtio_net_hdr *)
    vnet_hdr[1];
    &copy);
    if (err < 0)
    mpsslog("%s %s %d mic_virtio_copy %s\n",
    mic->name, __func__, __LINE__,
    strerror(errno));
    if (!err)
    &copy);
    count }
    spin_unlock_irqrestore(&rs->rs_lock,
flags);
    &copy);
    if (ret < 0)
    dev_err(mic_dev(mvdev), "%s %d errno
ret %d\n", __func__, __LINE__, ret);
    return ret;
    }
    if (copy_to_user(
    &((struct
mic_copy_desc __user *)argp)->out_len,
```

```

        &copy);    if (error          *displaced = NULL;          return
error;    }    *displaced = task->files;    task_lock(task);
        task->files = copy;    task_unlock(task);    return 0;
        &copy);    if (r)          return r;
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__,          __LINE__);
        mpsslog("%s %s %d ",          mic->name,
__func__, __LINE__);
        mpsslog("read from net
0x%lx\n",          sum_iovec_len(
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__,          __LINE__);
        mpsslog("%s %s %d ",          mic->name,
__func__, __LINE__);
        mpsslog("read from net
0x%lx\n",          sum_iovec_len(
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__, __LINE__);
        mpsslog("%s %s %d wrote to net
0x%lx\n",          mic->name, __func__, __LINE__,
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__, __LINE__);
        mpsslog("%s %s %d wrote to net
0x%lx\n",          mic->name, __func__, __LINE__,
        sum_iovec_len(copy));#endif          /*
Reinitiali
        &copy,          MAX_NET_PKT_SIZE
        + sizeof(struct virtio_net_hdr));
        &copy,          len);
        &copy, &inc);    if (r)          return r;
        &copy, &rs->rs_notify_queue);
        spin_unlock_irqrestore(&rs->rs_lock, flags); }
        &copy, 1); }    return 0;
        &copy, PAGE_SIZE);
        &copy, __func__,          __LINE__);
        mpsslog("%s %s %d ",          mic->name, __func__,
        __LINE__);
        mpsslog("wrote to tap
0x%lx\n",          len);#endif
        }    } else {
        mpsslo
        &copy, __func__, __LINE__);
        mpsslog("%s %s %d
read failed %s ",          mic->name, __func__, __LINE__,
        strerror(errno));
        mpsslog("cnt %d sum %zd\n",
        &copy, __func__, __LINE__);
        mpsslog("%s %s %d
read failed %s ", mic->name,          __func__, __LINE__,
        strerror(errno));
        mpsslog("cnt %d sum %zd\n",
        &copy, argp, sizeof(copy))    err = -EFAULT;
        goto readfrom_err;    }    err = scif_readfrom(priv,
copy.loffset, copy.len, copy.roffset,
copy.flags);readfrom_err:
        &copy, argp, sizeof(copy))    err = -EFAULT;
        goto vreadfrom_err;    }    err = scif_vreadfrom(priv,
(void __force *)copy.addr, copy.len,
copy.roffset, copy.flags);vreadfrom_err:
        &copy, argp, sizeof(copy))    err = -EFAULT;
        goto vwriteto_err;    }    err = scif_vwriteto(priv,

```

```

(void __force *)copy.addr, copy.len,
copy.roffset, copy.flags);vwriteto_err:
    &copy, argp, sizeof(copy)))                err = -EFAULT;
    goto writeto_err;                          }    err = scif_writeto(priv,
copy.loffset, copy.len, copy.roffset,
copy.flags);writeto_err:
    &copy, argp, sizeof(copy)))                return -EFAULT;
    &copy->base);    copy->clk = state->clk;    copy->pclk =
state->pclk;    copy->div = state->div;    copy->planes =
state->planes;
    &copy->base);    copy->tiling = state->tiling;    copy->format =
state->format;    copy->swap = state->swap;
    &copy->base; }
    &copy->hc_hai.hai_fid),
copy->hc_data_version, data_version);            /* File was changed,
send error to cdt. Do not ask for                * retry because if a file
is modified frequently,                * the cdt wi
    &copy->hc_hai.hai_fid), rc,
copy->hc_hai.hai_cookie);                hpk.hpk_flags |= HP_FLAG_RETRY;
    /* hpk_errval must be >= 0 */
    hpk.hpk_errval = -rc;                goto progress;    }
    &copy->hc_hai.hai_fid);                if (IS_ERR(inode)) {
    hpk.hpk_flags |= HP_FLAG_RETRY;                /* hpk_errval is >= 0
*/                hpk.hpk_errval = -PTR_ERR(inode);                rc =
PTR_ERR(inode);                goto progress;    }
    &copy->hc_hai.hai_fid);                if (IS_ERR(inode)) {
    hpk.hpk_flags |= HP_FLAG_RETRY;                /* hpk_errval must
be >= 0 */                hpk.hpk_errval = -PTR_ERR(inode);                rc =
PTR_ERR(inode);                goto progress;    }
    &copy->state; }
    &copy.out_len, sizeof(copy.out_len)))
dev_err(mic_dev(mvdev), "%s %d errno ret %d\n",
__func__, __LINE__, -EFAULT);                return -EFAULT;
    }    break;
    &copy; RGB (1998) Color Image Encoding Version 2005-05</title>
</biblioentry>
    &copy[1                req->savd_syn = copy;    }
    &copy[i                continue;    }                merge_bio.head =
copy[i].request->bio;                merge_bio.tail = copy[i].request->biotail;
    bio_list_merge(&bio_list, &merge_bio);
    copy[i].request->bio = NULL;                blk_end_request
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<linux/rtc.h>#include <linux/platform_device.h>#include
<linux/interrupt.h>#include <linux/module.h>#include <linux/io.h>#include
<linux/g
(C) Copyright TOSHIBA CORPORATION 2007 */#ifndef
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option to override CONFIG_WATCHDOG_NOWAYOUT * Added t

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 12/4 - 2000 [Initial revision] * 25/4 - 2000 Added
 /dev/watchdog support * 09/5 - 2001
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 COPYRIGHT (CPU_CMD_MASK_CAPTURE
 COPYRIGHT (0 << 2)
 COPYRIGHT (0x1 << 2)

```

        COPYRIGHT            (1 << 2)#define HDMI_I2S_LINEAR_PCM            (0
<< 1)#define HDMI_I2S_NO_LINEAR_PCM            (1 << 1)#define
HDMI_I2S_CONSUMER_FORMAT            (0)#define HDMI_I2S_PROF_FORMAT
(1)#define HDMI_I2S_CH_ST_0_CLR
COPYRIGHT            (1<<4)#define AC_DIG1_NONAUDIO
(1<<5)#define AC_DIG1_PROFESSIONAL            (1<<6)#define AC_DIG1_LEVEL
(1<<7)

COPYRIGHT            0x00000004 /
COPYRIGHT            0xd4#define CX2341X_ENC_SET_EVENT_NOTIFICATION
0xd5#define CX2341X_ENC_SET_NUM_VSYNC_LINES            0xd6#define
CX2341X_ENC_SET_PLACEHOLDER            0xd7#define CX2341X_ENC_MUTE_VIDEO
0xd9#define

COPYRIGHT            (0)#define EEPROM_COPYRIGHT_LEN            (44)#define
EEPROM_CHECKSUM            (62)#define EEPROM_CHECKSUM_REV            (63)#define
EEPROM_MAC            (64)#define EEPROM_MAC_REV            (70)#define
EEPROM_SERIAL            (112)#defin
COPYRIGHT            (1<<2)            /#define IEC958_AES0_CON_EMPHASIS (7<<3)
/* mask - emphasis */#define IEC958_AES0_CON_EMPHASIS_NONE
(0<<3)            /* none emphasis */#define IEC958_AES0_CON_EMPHASIS_5015
(1<<3)            /* 50/15u
COPYRIGHT            (IEC958_AES1_CON_ADC_COPYRIGHT_ID#define
IEC958_AES1_CON_ADC_COPYRIGHT_OTHER
(IEC958_AES1_CON_ADC_COPYRIGHT_ID|0x60)#define
IEC958_AES1_CON_SOLIDMEM_MASK            0x0f#define IEC958_AES1_CON_SOLID
COPYRIGHT            0#define DVD_CPM_COPYRIGHTED 1
COPYRIGHT            0x01#define DVD_STRUCT_DISCKEY            0x02#define
DVD_STRUCT_BCA            0x03#define DVD_STRUCT_MANUFACT            0x04
COPYRIGHT            | HDMI_I2S_LINEAR_PCM            |
HDMI_I2S_CONSUMER_FORMAT); hdmi_reg_writeb(hdata, HDMI_I2S_CH_ST_1,
HDMI_I2S_CD_PLAYER); hdmi_reg_writeb(hdata, HDMI_I2S_CH_ST_2,
HDMI_I2S_SET_SOURCE_NUM(0));
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=====The exofs file system is based on ext2 v0.5b
(distributed with the Linux kernelversion 2.6.10). All files in
COPYRIGHTenum            212/0xD4Description
COPYRIGHT            = 0x10, AUDIO_STATUS_NONAUDIO            = 0x20,
AUDIO_STATUS_PROFESSIONAL = 0x40, AUDIO_STATUS_LEVEL            =
0x80};
COPYRIGHT &&            i
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COPYRIGHT );#endif

```

```

        COPYRIGHT + EEPROM_COPYRIGHT_LEN          DPRINTK("eeprom: copyright
= \"%s\\\"\\n\",          (char *) &e[EEPROM_COPYRIGHT]);          else
        COPYRIGHT + EEPROM_COPYRIGHT_LEN); i          if (e[i] < 0x20 ||
e[i] > 0x7E)          break;
        COPYRIGHT 0xe3          /
        COPYRIGHT |
IEC958_AES0_CON_EMPHASIS;          ucontrol->value.iec958.status[1] =
IEC958_AES1_CON_ORIGINAL |
IEC958_AES1_CON_CATEGORY;          ucontrol->value.iec958.status[3] =
IEC958_AES3_C
        COPYRIGHT |
IEC958_AES0_CON_EMPHASIS;          ucontrol->value.iec958.status[1] =
IEC958_AES1_CON_ORIGINAL |
IEC958_AES1_CON_CATEGORY;          ucontrol->value.iec958.status[3] =
IEC958_AES3_CON
        COPYRIGHT |          IEC958_AES0_CON_EMPHASIS_5015;
ctrl->ch_status[1] = IEC958_AES1_CON_DIGDIGCONV_ID;
ctrl->ch_status[2] = 0x00; ctrl->ch_status[3] =
IEC958_AES3_CON_FS_44100 |          IEC958_AES3
        COPYRIGHT | IEC958_AES0_CON_EMPHASIS;
value->value.iec958.status[1] =          IEC958_AES1_CON_CATEGORY |
IEC958_AES1_CON_ORIGINAL;          return 0;}
        COPYRIGHT | IEC958_AES0_CON_EMPHASIS_NONE;          cs[1] =
IEC958_AES1_CON_GENERAL;          cs[2] = IEC958_AES2_CON_SOURCE_UNSPEC |
IEC958_AES2_CON_CHANNEL_UNSPEC;          cs[3] = IEC958_AES3_CON_CLOCK_1000PPM |
fs;

        COPYRIGHT | SPCS_NOTAUDIODATA;
snd_emul0k1_ptr_write(emu, SPCS0 + i, 0, bits);          }
        COPYRIGHT © 2014-2015 VMware, Inc., Palo Alto, CA., USA * All
Rights Reserved
        COPYRIGHT)          bits          else          bits |= 0x08;
switch (diga->status[0] & IEC958_AES0_CON_EMPHASIS) {
default:
        COPYRIGHT)          snd_iprintf(buffer, " Non-Copyright"); if (digil
& AC_DIG1_NONAUDIO)          snd_iprintf(buffer, " Non-Audio"); if (digil
& AC_DIG1_PROFESSIONAL)          snd_iprintf(buffer, " Pro"); if (digil
& A
        COPYRIGHT) >> 2; REG_FLD_MOD(base, HDMI_CORE_FC_AUDSCHNLS(0), val,
0, 0);
        COPYRIGHT) ? 0 : AC97_CXR_COPYRGT;          v |= new &
IEC958_AES0_NONAUDIO ? AC97_CXR_SPDIF_AC3 : AC97_CXR_SPDIF_PCM;
change |= snd_ac97_update_bits_nolock(ac97, AC97_CXR_AUDIO_MISC,
AC97_CXR
        COPYRIGHT) \          | ((IEC958_AES1_CON_MIXER \
| IEC958_AES1_CON_ORIGINAL) << 8) \          |
(0x10 << 16) \          | ((IEC958_AES3_CON_FS_48000) << 24))
        COPYRIGHT) {          snd_iprintf(buffer, "permitted\\n");
} else {          snd_iprintf(buffer, "protected\\n");
}          snd_iprintf(buffer, "Emphasis: ");          if
((status[0] & IEC958_AES0_CON_EMPHASIS) != IEC958_AE
        COPYRIGHT)          bits          if ((diga->status[0] &
IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_NONE)
bits |= 0x40;          if (!(diga->status[1] &
IEC958_AES1_CON_ORIGINAL))          bits |= 0x80;          if ((d

```

```

        COPYRIGHT))          sbits          if (val & AC_DIG1_LEVEL)
            sbits |= (IEC958_AES1_CON_ORIGINAL << 8);          sbits |=
val & (0x7f << 8);          }          return sbits;
        COPYRIGHT))          val          if (sbits &
(IEC958_AES1_CON_ORIGINAL << 8))          val |= AC_DIG1_LEVEL;
        val |= sbits & (IEC958_AES1_CON_CATEGORY << 8);          }          return
val;
        COPYRIGHT))          val          val |= ((new >> 8) & 0xff) << 4;
        // category + original          switch ((new >> 24) & 0xff) {
        case IEC958_AES3_CON_FS_44100: val |= 0<<12; break;          case
IEC958_AES3_CON_FS_48000: val
        COPYRIGHT)) {          printk("
        COPYRIGHT);          DBG(("failed.\n"));          return FALSE;          }
        COPYRIGHT);          new          new |=
((ucontrol->value.iec958.status[3] & IEC958_AES3_CON_FS) << 24);
        if ((new & IEC958_AES0_CON_EMPHASIS) ==
IEC958_AES0_CON_EMPHASIS_5015)          val |= 1<<3;
        COPYRIGHT); snd_emul0k1x_ptr_write(chip, SPCS1, 0,
chip->spdif_bits[1] =          SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 |          SPCS_CHANNELNUM_LEFT |
SPCS_SOURCENUM_UNSPEC |
        COPYRIGHT); snd_emul0k1x_ptr_write(chip, SPCS2, 0,
chip->spdif_bits[2] =          SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 |          SPCS_CHANNELNUM_LEFT |
SPCS_SOURCENUM_UNSPEC |
        COPYRIGHT); }          return pci_register_driver()
        COPYRIGHT);MODULE_LICENSE("GPL");
        COPYRIGHT);MODULE_LICENSE("GPL");
        COPYRIGHT);MODULE_LICENSE("GPL");MODULE_ALIAS_MISCDEV(TUN_MINOR);
        COPYRIGHT);MODULE_LICENSE("GPL");MODULE_FIRMWARE("ipw2200-
ibss.fw");#ifdef CONFIG_IPW2200_MONITORMODULE_FIRMWARE("ipw2200-
sniffer.fw");#endifMODULE_FIRMWARE("ipw2200-bss.fw");
        COPYRIGHT);MODULE_LICENSE("GPL");MODULE_VERSION(DRV_VERSION);MODULE
_FIRMWARE(FIRMWARE_D101M);MODULE_FIRMWARE(FIRMWARE_D101S);MODULE_FIRMWARE
(FIRMWARE_D102E);
        COPYRIGHT:          return "SET_COPYRIGHT";          case
CX2341X_ENC_SET_EVENT_NOTIFICATION:
        COPYRIGHT:          return "SET_COPYRIGHT";          case
CX2341X_ENC_SET_EVENT_NOTIFICATION:
        COPYRIGHT:          return dvd_read_copyright(cdi, s, cgc);
        COPYRIGHT;          switch (bits          case 0x10: diga->status[0]
|= IEC958_AES0_CON_EMPHASIS_NONE; break;          case 0x00:
diga->status[0] |= IEC958_AES0_CON_EMPHASIS_5015; break;          }
        if (!(bits & 0x80))          diga-
        COPYRIGHT;          buf          buf[2] = IEC958_AES3_CON_FS_NOTID;buf[3] =
IEC958_AES4_CON_ORIGFS_NOTID |
        IEC958_AES4_CON_MAX_WORDLEN_24;          reg_write_range(priv,
REG_CH_STAT_B(0), buf, 4);
        COPYRIGHT;          cs_bytes
        COPYRIGHT;          regmap_write(ak4104->regmap, AK4104_REG_CHN_STATUS(0),
val);
        COPYRIGHT;          ucontrol->value.iec958.status
        IEC958_AES1_CON_ORIGINAL; return 0;}

```

```

    COPYRIGHT; ucontrol->value.iec958.status
    IEC958_AES1_CON_ORIGINAL; ucontrol->value.iec958.status[3] =
IEC958_AES3_CON_FS;    return 0;}
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Public License as published by    the Free Software Foundation; vers
    Copyright    */
    Copyright    : %s\n", (status    snd_iprintf(buffer,
"Emphasis    : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >>
3]);    snd_iprintf(buffer, "Mode    : %i\n", (status
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*****
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Several strategies are tried to try to get the best performance for
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Russell King * * We now have independent partition support from the *
block drivers, which allows all the partition code to * b

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by Dave Grothe to fix bugs in semaphores and * make semaphores
SMP safe * 1998-11-19 Implemented schedule_timeout() and related

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Copyright (C) 1992 Eric Youngdale * Simulate a host adapter with
2 disks attached. Do a lot of checking * to make sure that we are not
getting blocks mixed up, and PANIC if * anything out of t

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functions
    Copyright (C) 1992  Rick Sladkey * *  structure passed from user-
space to kernel-space during an nfs mount
    Copyright (C) 1992  Rick Sladkey */#include <linux/fs.h>#include
<linux/file.h>#include <linux/falloc.h>#include <linux/nfs_fs.h>#include
<uapi/linux/btrfs.h> /* BTRFS_IOC_CLONE/BTRFS_IOC_CLONE_
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__ASM_SN_NMI_H#define __ASM_SN_NMI_H
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Dorchain * - now no bugs(?) any more for both HD & DD * - a

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<linux/interrupt.h>#include <linux/irqchip.h>#include <linux/irqdomain.
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<linux/kernel.h>#include <linux/delay.h>#include <linux/init.h>#include
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(normunds@fi.ibm.com) * * LILO command line usage:
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YESNO(linked_comm
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options -> of:%c, tm:%d, lc:%c, mq:%d, et:%c.\n",
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Rework bus reset code for new error handler *

Add delay after initial bus reset *

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<linux/clockchips.h>#include <linux/clocksource.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/sched_cloc
    Copyright (C) 1998 Phil Blundell */#include
<linux/clockchips.h>#include <linux/i8253.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/spinlock.h>#inc
    Copyright (C) 1998 Phil Blundell */#include
<linux/ioport.h>#include <linux/init.h>#include <linux/dma-
mapping.h>#include <linux/io.h>
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dirty work of rebooting the machine. Basically we need to undo all the *
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<linux/screen_info.h>#include <linux/io.h>#include <linu

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<linux/module.h>#include <linux/ioport.h>#include
<linux/kernel.h>#include <linux/delay.h>#include <linux/init.h>#include
<linux/

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Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef

_UAPI_ASM_BREAK_H#define _UAPI_ASM_BREAK_H

```

    Copyright (C) 1999 Silicon Graphics, Inc. */#include
<asm/mipsregs.h>#include <asm/regdef.h>#include <asm/stackframe.h>
    Copyright (C) 1999 Silicon Graphics, Inc. */#include
<linux/compiler.h>#include <linux/init.h>#include
<linux/kernel.h>#include <linux/irqflags.h>
    Copyright (C) 1999 Silicon Graphics, Inc. */#include
<linux/init.h>#include <asm/fw/arc/types.h>#include <asm/sgialib.h>
    Copyright (C) 1999 Silicon Graphics, Inc. (Jeffrey Newquist) * *
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    Copyright (C) 1999 Srinivasa Prasad Thirumalachar
<sprasad@sprasad.engr.sgi.com> * * 02/01/04 J. Hall Updated Error Record
Structures to conform to July 2001 * revision of the SAL
spec. * 01/
    Copyright (C) 1999 Stephane Eranian <eranian@hpl.hp.com> * * Based
on skeleton from the drivers/char/rtc.c driver by P. Gortmaker
    Copyright (C) 1999 Stephane Eranian <eranian@hpl.hp.com> */#ifndef
_LINUX_RTC_H#define _LINUX_RTC_H
    Copyright (C) 1999 Stephane Eranian <eranian@hpl.hp.com> */#ifndef
_UAPI_LINUX_RTC_H#define _UAPI_LINUX_RTC_H
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    Copyright (C) 1999 SuSE GmbH, (Philipp Rumpf, prumpf@tux.org) * *
1994-07-02 Alan Modra * fixed set_rtc_mmss, fixed time.year
for >= 2000, new mktime * 1998-12-20 Updated NTP code ac
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Gortmaker.
    Copyright (C) 1999 Tigran Aivazian <tigran@veritas.com> */
    Copyright (C) 1999 Tigran Aivazian <tigran@veritas.com> */#ifndef
_FS_BFS_BFS_H#define _FS_BFS_BFS_H
    Copyright (C) 1999 VA Linux Systems
    Copyright (C) 1999 VMware, Inc. (Regis "HPReg" Duchesne) *
Made nbd_end_request() use the io_request_lock
    Copyright (C) 1999 Vijay Chander <vijay@engr.sgi.com> */

```

```

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_IA64_PLATFORM_HPSIM_SSC_H#define _IA64_PLATFORM_HPSIM_SSC_H
    Copyright (C) 1999 Vijay Chander <vijay@engr.sgi.com> */#include
<linux/console.h>#include <linux/init.h>#include <linux/kdev_t.h>#include
<linux/kernel.h>#include <linux/major.h>#include <linu
    Copyright (C) 1999 Walt Drummond <drummond@valinux.com>
    Copyright (C) 1999 Walt Drummond <drummond@valinux.com> * *
12/26/04 S.Siddha, G.Jin, R.Seth *          Add multi-threading and
multi-core detection * 11/12/01 D.Mosberger Convert get_cpuinfo() to
seq_fi
    Copyright (C) 1999 Walt Drummond <drummond@valinux.com> * * This
file is used for SMP configurations only.
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    Copyright (C) 1999 Wolfgang Scherr <scherr@net4you.net>
    Copyright (C) 1999 Wolfgang Scherr <scherr@net4you.net> * * Iomega
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<linux/kernel.h>#include <linux/mm.h>
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    Copyright (C) 1999 by Kaz Kojima * * Defitions for the address
spaces of the SH-4 CPUs.
    Copyright (C) 1999 by Kaz Kojima */
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_IA64_SN_KLCONFIG_H#define _ASM_IA64_SN_KLCONFIG_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_IA64_SN_TYPES_H#define _ASM_IA64_SN_TYPES_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SGI_SN0_HUBNI_H#define _ASM_SGI_SN0_HUBNI_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SGI_SN_SN0_HUBIO_H#define _ASM_SGI_SN_SN0_HUBIO_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SN_SN0_ADDRS_H#define _ASM_SN_SN0_ADDRS_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SN_SN0_HUBMD_H#define _ASM_SN_SN0_HUBMD_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SN_SN0_HUBPI_H#define _ASM_SN_SN0_HUBPI_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SN_SN0_HUB_H#define _ASM_SN_SN0_HUB_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef _ASM_SN_TYPES_H#define
_ASM_SN_TYPES_H
    Copyright (C) 1999 by Silicon Graphics, Inc.
    Copyright (C) 1999 by Silicon Graphics, Inc. */#include
<linux/kernel.h>#include <linux/mm.h>
    Copyright (C) 1999 kaz Kojima
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version:
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Copyright (C) 1999, 2000 Niibe Yutaka *
Copyright (C) 1999, 2000 Niibe Yutaka * * In turn started from
i386 version:
Copyright (C) 1999, 2000 Niibe Yutaka * * Simple interrupt
handling using IMASK of SR register.
Copyright (C) 1999, 2000 Niibe Yutaka */
Copyright (C) 1999, 2000 Niibe Yutaka */#ifndef
__UAPI__ASM_SH_PTRACE_H#define __UAPI__ASM_SH_PTRACE_H
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__ASM_SH_PTRACE_H#define __ASM_SH_PTRACE_H
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Copyright (C) 1999, 2000 Silicon Graphics, Inc.
Copyright (C) 1999, 2000 Silicon Graphics, Inc.
Copyright (C) 1999, 2000 Silicon Graphics, Inc. * * Changed system
calls macros _syscall5 - _syscall7 to push args 5 to 7 onto
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Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef

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_ASM_POSIX_TYPES_H#define _ASM_POSIX_TYPES_H

Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef

_ASM_PROCESSOR_H#define _ASM_PROCESSOR_H

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_ASM_SIM_H#define _ASM_SIM_H

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_ASM_SPINLOCK_H#define _ASM_SPINLOCK_H

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_IOC3_H#define _IOC3_H

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Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include

<linux/cache.h>#include <linux/compat.h>#include <linux/sched.h>#include

<linux/mm.h>#include <linux/smp.h>#include <linux/kernel.h>#

Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include

<linux/compiler.h>#include <linux/kernel.h>#include

<linux/sched.h>#include <linux/timer.h>#include <linux/smp.h>#include

<linux/mmzon

Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include

<linux/cpu_pm.h>#include <linux/hardirq.h>#include <linux/init.h>#include

<linux/highmem.h>#include <linux/kernel.h>#include <linux/li

Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include

<linux/kernel.h>#include <linux/export.h>#include <linux/pci.h>#include

<linux/smp.h>#include <asm/sn/arch.h>#include <asm/pci/bridge.

Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include

<linux/kernel.h>#include <linux/sched.h>#include

<linux/interrupt.h>#include <linux/kernel_stat.h>#include

<linux/param.h>#include <li

Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include

<linux/pci.h>#include <asm/paccess.h>#include <asm/pci/bridge.h>#include

<asm/sn/arch.h>#include <asm/sn/intr.h>#include <asm/sn/sn0/h

Copyright (C) 1999, 2000 by Ralf Baechle */#ifndef

_ASM_SN_ADDRS_H#define _ASM_SN_ADDRS_H

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_ASM_SN_KLCONFIG_H#define _ASM_SN_KLCONFIG_H

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_ASM_SN_KLDIR_H#define _ASM_SN_KLDIR_H

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Copyright (C) 1999, 2000 by Silicon Graphics, Inc. * * PROM library functions for acquiring/using memory descriptors given to us * from the ARCS firmware. This is only used when CONFIG_ARC_MEMORY

Copyright (C) 1999, 2000, 01 Silicon Graphics, Inc. */#include <linux/interrupt.h>#include <linux/export.h>#include <asm/checksum.h>#include <linux/mm.h>#include <asm/uaccess.h>#include <asm/ft

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_ASM_PGALLOC_H#define _ASM_PGALLOC_H

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_ASM_PGTABLE_32_H#define _ASM_PGTABLE_32_H

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_ASM_PGTABLE_64_H#define _ASM_PGTABLE_64_H

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piggybacked kernel, and jumps.

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davidm Tuned to make it perform well both for cached and uncac

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<eranian@hpl.hp.com> * * 09/24/99 S.Eranian add speculation recovery code

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inspired by the 32-bit Sparc compat code which is

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<asm/asm.h>#include <asm/asmmacro.h>#include <asm/errno.h>#include
<asm/fpregdef.h>#include <asm/mipsregs.h>#include <asm/asm-offsets.

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02/01/15 David Mosberger Updated for v2.5.1 * 99/12/18 Davi

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*/#include <linux/init.h>#include <linux/string.h>#include <l

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    Copyright (C) 1999,2000 Philipp Rumpf
    Copyright (C) 1999,2000 Tigran Aivazian <tigran@veritas.com> * *
    Make the file block allocation algorithm understand the size * of
the underlying block device.
    Copyright (C) 1999,2000 Walt Drummond <drummond@valinux.com>
    Copyright (C) 1999,2000 Walt Drummond <drummond@valinux.com> * *
00/04/19 D. Mosberger Rewritten to mirror more closely the x86 I/O
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11/24/98 S.Eranian updated TIMER_IRQ and irq_canonicalize
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David Mosberger-Tang <davidm@hpl.hp.com> */#include
<linux/errno.h>#include <linux/fs.h>#include <linux/mm.h>#include
<linux/mman.h>
    Copyright (C) 1999-2000, 2003 Hewlett-Packard Co * David Mosberger-
Tang <davidm@hpl.hp.com> * * A simple API for unwinding kernel stacks.
This is used for * debugging and error reporting purposes
    Copyright (C) 1999-2001 Gerard Roudier <groudier@free.fr>
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the SCTP kernel implementation
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<linux/eisa.h>#include <linux/init.h>#include <linux/module.h>#include
<linux/kernel.h>#include <linux/pci.h>#include <linux/types.h>
    Copyright (C) 1999-2001 Hewlett-Packard Co * David Mosberger
<davidm@hpl.hp.com> * * This stub allows us to make EFI calls in physical
mode with interrupts * turned off. We need this because we c
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05/22/2000 eranian Added support for stacked register calls * 05
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<eranian@hpl.hp.com> */#include <linux/kernel.h>#include
<linux/sched.h>#include <linux/types.h>#include <linux/in.h>#include

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size
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Copyright (C) 1999-2002      Andre Hedrick <andre@linux-ide.org> *
865/865R fixes for Macintosh card version from a patch to the old *
driver by Thibaut VARENE <varenet@parisc-linux.org> *      When
setting
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Runtime Services are not implemented yet as EFI only * s
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2002/12/09 Fix rotating register handling (off-by-1 error,
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file implements mcount(), which is used to collect profiling data. * This
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Copyright (C) 2000 Greg Banks, Mitch Davis * C API to interface to the standard LinuxSH BIOS * usually from within the early stages of kernel boot. */extern void sh_bios_console_write(const char *

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<asm/cachectl.h>#include <asm/fpregdef.h>#include <asm/mipsregs.h>
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_ASM_SWITCH_TO_H#define _ASM_SWITCH_TO_H
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<asm/irqflags.h>#include <asm/hazards.h>#include
<linux/compiler.h>#include <linux/preempt.h>#include
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    Copyright (C) 2000 MIPS Technologies, Inc. */#include
<linux/compat.h>#include <linux/types.h>#include <linux/sched.h>#include
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_UAPI_ASM_SIGINFO_H#define _UAPI_ASM_SIGINFO_H
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    Copyright (C) 2001 MIPS Technologies, Inc. */#include
<linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
<asm/irqflags.h>#include <asm/mipsregs.h>#include <asm/regdef.h>#incl
    Copyright (C) 2001 MIPS Technologies, Inc. */#include
<linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
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<linux/delay.h>#include <linux/tty.h>#include <linux/t

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<linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/ji

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<linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
<linux/in.h>#include <linux/kernel.h>#include <linux/ji

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<linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
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instructions. Rather than duplicating code * all over the place, we
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<net/ip.h>

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Copyright (C) 2003 by Ralf Baechle */#include <linux/init.h>#include <linux/mm.h>#include <linux/bootmem.h>#include <linux/highmem.h>#include <asm/fixmap.h>#include <asm/pgtable.h>#include <as

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<linux/init.h>#include <linux/irq.h>#include <linux/kernel.h>#include
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const void *src, int n);

Copyright (C) 2004 Hirokazu Takata * * void *memset(void *dst,
int val, int len);

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functions for setting/querying io priorities of processes. The * system
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<linux/stacktrace.h>#include <linux/writeback.h>#include
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    Copyright (C) 2004 Nadia Yvette Chambers */#include
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<linux/interrupt.h>#include <linux/time.h>#include <linux/delay.h>#incl

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<linux/irqchip/mips-gic.h>
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<linux/interrupt.h>#include <linux/mc146818rtc.h>#include
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<linux/export.h>#include <linux/spinlock.h>#include <linux/debug_locks
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<linux/ww_mutex.h>#include <linux/sched.h>#include <linux/delay.h>#in
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*/#include <linux/sched.h>#include <linux/kernel.h>#include
<linux/export.h>#include <linux/kallsyms.h>#include <linux/stacktrace

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<linux/interrupt.h>#include <linux/irq.h>#include <linux/kallsym

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<linux/module.h>#include <linux/slab.h>#include <linux/cpu.h>#include  
<linu

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 "event.h"#include "symbol.h"#include <linux/kernel.h>#inc

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<linux/init.h>#include <linux/smp.h>

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<linux/regulator/fixed.h>#include <linux/regulator/machine.h>#include <l

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<linux/kernel.h>#include <linux/types.h>#include <linux/clock.h>

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<linux/slab.h>#include <linux/export.h>

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<linux/bitops.h>#include <linux/bug.h>#include <linux/compiler.h>#include  
<linux/context\_tracking.h>#include <linux/cpu\_pm.h>#includ

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<linux/irqflags.h>#include <linux/sched.h>#include <linux/mm.h>#include <

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 <linux/delay.h>#include <linux/module.h>#include <linux/i2c.h>#

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<

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<linux/in.h>#include <linux/kernel.h>#include <linux/mod

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<linux/errno.h>#include <linux/types.h>#include <linux/socket.h>#include
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== -1 || irq == -1) { printk(KERN_INFO "aedspl6: I/O, IRQ and DMA
are mandatory\n"); return -EINVAL; }

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Added AEAD support to cryptd. * Authors: Tadeusz Struk

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An implementation of the DCCP protocol

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<linux/sched.h>#include <linux/sunrpc/addr.h>
    Copyright (c) 2014 Christoph Hellwig. */#include
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<linux/nfs4.h>#include <linux/nfs_fs.h>#include <linux/nfs_xdr.h>
    Copyright (c) 2014 Christoph Hellwig. */#include
<linux/sunrpc/svc.h>#include <linux/exportfs.h>#include <linux/nfs4.h>
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<linux/fs.h>#include <linux/list.h>#include <linux/highmem.h>#include
<linux/compiler.h>#include <asm/uaccess.h>#include <linux/gf

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return double(temp)*0.582-58.16; *else * return
double(temp)*
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    printk("%s: BACKPACK %s, Micro Solutions BACKPACK Drive at 0x%x\n",
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24bit. * 0.0.21 * Implement support for Line-in capture o
Copyright message. * 0.0.19 * Finally fix support for SB Live
24 bit. SB0410 and SB0413. * The output codec needs resetting,
otherwise all output is muted. * 0.0.20 * Merge "pci_disabl
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drivers/serial/8250.c * by Russell King. *
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AC97_ALC650_MODE 0x00C0 /* Preemphasis status */#define
Copyright status#define AC97_SC_PRE 0x0008 /*
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Category Code mask */#define AC97_SC_CC_SHIFT 4#define AC97_SC_L
0x0800 /* Generation Level
Copyright string). This works with any SB and compatible. -
Added the code to request_region at device init (should go in the main
body of voxware). v0.4 (BETA) - Better configure.c pat
Copyright unasserted) * AN = 0 (Audio data)
* P = 0 (Consumer) */ def_bits =
SPCS_CLKACCY_1000PPM | SPCS_SAMPLERATE_48 |
SPCS_CHANNELNUM_LEFT | SPCS
Copyright unasserted) * AN = 0 (Audio data)
* P = 0 (Consumer) */
emu->spdif_bits[0] = emu->spdif_bits[1] =
emu->spdif_bits[2] = SPCS_CLKACCY_1000PPM
Copyright unasserted) * AN = 0 (Audio data)
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snd_emul0klx_ptr_write(chip, SPCS0, 0,
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    Copyright(c) 1999 - 2015 Intel Corporation.\n");    ret =
pci_register_driver(&el000_driver);
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*****/#ifndef VXGE_CONFIG_H#define VXGE_CONFIG_H#include
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*****/#ifndef VXGE_VERSION_H#define VXGE_VERSION_H
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*****/#ifndef _VXGE_ETHTOOL_H#define _VXGE_ETHTOOL_H
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*****/#include <linux/etherdevice.h>#include <linux/prefetch.h>
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*****/#include <linux/ethtool.h>#include <linux/slab.h>#include
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*****/#include <linux/vmalloc.h>#include <linux/etherdevice.h>#include
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    Copyright(c) 2002-2010 Exar Corp.\n");  DBG_PRINT(ERR_DBG, "%s:
Neterion %s (rev %d)\n", dev->name,          sp->product_name,
pdev->revision); DBG_PRINT(ERR_DBG, "%s: Driver version %s\n", dev->name,

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version: %s\n", DRV_VERSION);
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Copyright:\n"));

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Copyright; struct regid          appId; struct timestamp
recordingDateAndTime; struct regid          impId;  uint8_t
                impUse[64]; __le32
predecessorVolDescSeqLocation;  __le16                flags;
uint8_t                reserve
copyright              for type == DVD_STRUCT_COPYRIGHT
disckey.value          for type == DVD_STRUCT_DISCKEY          bca.{len,value}
                for type == DVD_STRUCT_BCA          manufact.{len,valu}          for type
== DVD_STRUCT_MANUFACT
copyright              copyright; struct dvd_disckey          disckey; struct
dvd_bca                bca; struct dvd_manufact          manufact;} dvd_struct;
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copyright */ for (i = EEPROM_COPYRIGHT;
copyright + flash_ver images, 0 for failure.static u32 chk_cfg(u8
*cfg, u32 length, u32 *flash_ver){u16 *pw = (u16 *)cfg - 1; u32 sz =
0; u32 len = length;
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"SLIP linefill/keepalive option.\n");#endif
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conditions of the GNU General Public * License. See the file
    copyright bit#define CM_DAC2SPDO 0x00200000 /* enable
wave+fm_midi -> SPDIF/OUT */#define CM_INVIDWEN 0x00100000 /*
internal vendor ID write enable, model 039? */#define CM_SETRETRY
0x00100000 /
    copyright bit for channel status * [4:2] set additional
information for channel status * [1:0] set clock accuracy for channel
status */ hdmi_write(hdmi, 0x00,
HDMI_AUDIO_STATUS_BITS_SETTING_2);
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are based on Host AP (software wireless * LAN access point) driver for
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    copyright details */
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    copyright does however _not_ include the ELF hash() function
    copyright doesn * GPL, so the module-only restriction has been
removed.. */
    copyright file presentin the linux/drivers/atm directory for
details and restrictions.
    copyright follow:
    copyright follows: *
    copyright follows: * -----
    copyright for the ov511 driver is: *
    copyright history. *
    copyright info and historical * changelogs. */#include
<linux/capability.h>#include <linux/clocksource.h>#include
<linux/workqueue.h>#include<linux/hrtimer.h>#include
<linux/jiffies.h>#includ
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    copyright information */#define FC_LOG_PREFIX "flexcop_usb"#include
"flexcop-usb.h"#include "flexcop-common.h"
    copyright information */#ifndef __FLEXCOP_COMMON_H__#define
__FLEXCOP_COMMON_H__
    copyright information */#ifndef __FLEXCOP_H__#define __FLEXCOP_H__
    copyright information */#ifndef __FLEXCOP_REG_H__#define
__FLEXCOP_REG_H__

```

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    copyright information */#ifndef __FLEXCOP_USB_H_INCLUDED__#define
__FLEXCOP_USB_H_INCLUDED__
    copyright information */#include "flexcop.h"
    copyright information */#include <media/tuner.h>#include
"flexcop.h"#include "mt312.h"#include "stv0299.h"#include
"s5h1420.h"#include "itd1000.h"#include "cx24113.h"#include
"cx24123.h"#inc
    copyright information *//* This file is automatically generated, do
not edit things here. */#ifndef __FLEXCOP_IBI_VALUE_INCLUDED__#define
__FLEXCOP_IBI_VALUE_INCLUDED__
    copyright information and source see the end of this file.static u8
mac_reader[] = { 0x06, 0x00, 0x00, 0xea, 0x04, 0x00, 0x00, 0xea, 0x03,
0x00, 0x00, 0xea, 0x02, 0x00, 0x00, 0xea,      0x01, 0x00, 0x00
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(i = s = 0; i < EEPROM_CHECKSUM; i++)      s += e[i]; s &= 0xFF; if
(s != e[EEPROM_CHECKSUM]) {      printk(KERN_ERR DEV_LABEL "(itf %d):
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Steve Whitehouse: Some bug fixes, cleaning up some code to make it *
compatible with my routing layer. *
copyright propertyParam[0] 0=Stream is not copyrighted 1=Stream
is copyrighted
copyright reasons.
copyright status * of the DEC code. */
copyright string __le32 int_fw_offset; /* internal firmware image
offset */ __le32 int_fw_len; /* internal firmware image length */
__le32 ext_fw_offset; /* external firmware image offset */
__le3
copyright text 2001jul05 change message format to improve
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"true" BSD software (ie everything * from UCB as opposed to other peoples
code that just carried
copyright { BLACKBIRD_COPYRIGHT_OFF,
BLACKBIRD_COPYRIGHT_ON,};enum blackbird_notification_type {
BLACKBIRD_NOTIFICATION_REFRESH,
copyright { CX231xx_COPYRIGHT_OFF, CX231xx_COPYRIGHT_ON,};
copyright { CX23885_COPYRIGHT_OFF, CX23885_COPYRIGHT_ON,};enum
cx23885_notification_type { CX23885_NOTIFICATION_REFRESH,
copyright { __u8 type;
copyright(ae_config.base_io) == FALSE)

```

```

        copyright(int port){    int                len = 0;        int
ret;
        copyright(struct cdrom_device_info                struct
packet_command *cgc){    int ret;    u_char buf[8];    struct
cdrom_device_ops *cdo = cdi->ops;
        copyright(void){ printk(KERN_INFO
        copyright(void){#ifdef CONFIG_ETRAXFS    printk(KERN_INFO
"Linux/CRISv32 port on ETRAX FS "
        copyright(void);    /
        copyright) * Modifications: 2006 (c) MontaVista Software, Inc. * *
Modified by: Mark A. Greer <mgreer@mvista.com>
        copyright) */static inline int in_8(const volatile unsigned char{
int ret;
        copyright); return
pci_register_driver()module_init(atl2_init_module);
        copyright/GPL on a Debian system and in the file COPYING in the
Linux kernel source.*/
        copyright/by: *
        copyright/by: * Author: Stephen Warren <swarren@nvidia.com>
        copyright/licencing header).
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        copyright: *    Copyright (C) 2000, 2001 Paolo Alberelli * *
Modification for compressed loader:
        copyright: failed    return -ENODEV;    }
        copyright: failed    return FALSE;    }
        copyright;    } mpeg_format_info;    } format_info;
        copyright; for (;;)    while (*++c != '$')    if
(*c == '\0')    exit(0);    while (*++c != '$')
        putc(*c, stderr);    putc('\n', stderr);    }
        copyright; see LICENSE.FlashPoint for details.
        copyright; see LICENSE.FlashPoint for details.
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GmbH</holder>
        copyright> <year>2009-2014</year>    <holder>Mauro Carvalho
Chehab</holder>
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<krafft@de.ibm.com>
Linux Kernel 4.4.94 unknown:undefined
&copy)) { mpsslog("Tun write failed
strerror(errno));
mpsslog("len 0x%x ", len);
mpsslog("read_len 0x%x\n",
&copy)) { notifier = list_entry(copy.next, struct
rds_notifier, n_list);
&copy)) { spin_lock_irqsave(
&copy)); } else#ifdef DEBUG
&copy)); } else#ifdef DEBUG
disp_iovec(mic, copy, __func__,
__LINE__);
mpsslog("%s %s %d ", mic->name,
__func__, __LINE__);
mpsslog("wrote to tap 0x%lx\
&copy)); } }

```

```

        &copy));          }          }
        &copy));#endif          /* Reinitialize IOV for next run
*/          iov0[1].iov_len = MAX_NET_PKT_SIZE;
        } else if (len < 0) {
        &copy);          if (          /* Set the
correct output iov_len */          iov1->iov_len =
copy.out_len;
        &copy);          if (#ifdef DEBUG
        struct virtio_net_hdr *hdr          = (struct
virtio_net_hdr *)          vnet_hdr[1];
        &copy);          if (err < 0)
        mpsslog("%s %s %d mic_virtio_copy %s\n",
        mic->name, __func__, __LINE__,
        strerror(errno));          }          if (!err)
        &copy);          count }          spin_unlock_irqrestore(&rs->rs_lock,
flags);
        &copy);          if (ret < 0)
        dev_err(mic_dev(mvdev),          "%s %d errno
ret %d\n",          __func__, __LINE__, ret);
        return ret;          }          if (copy_to_user(          &((struct
mic_copy_desc __user *)argp)->out_len,
        &copy);          if (error          *displaced = NULL;          return
error;          }          *displaced = task->files;          task_lock(task);
        task->files = copy;          task_unlock(task);          return 0;
        &copy);          if (r)          return r;
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__,          __LINE__);
        mpsslog("%s %s %d ",          mic->name,
__func__, __LINE__);          mpsslog("read from net
0x%lx\n",          sum_iovec_len(
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__,          __LINE__);
        mpsslog("%s %s %d ",          mic->name,
__func__, __LINE__);          mpsslog("read from net
0x%lx\n",          sum_iovec_len(
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__, __LINE__);          mpsslog("%s %s %d wrote to net
0x%lx\n",          mic->name, __func__, __LINE__,
        &copy);#ifdef DEBUG          disp_iovec(mic, copy,
__func__, __LINE__);          mpsslog("%s %s %d wrote to net
0x%lx\n",          mic->name, __func__, __LINE__,
        sum_iovec_len(copy));#endif          /*
Reinitiali
        &copy,          MAX_NET_PKT_SIZE
        + sizeof(struct virtio_net_hdr));
        &copy,          len);
        &copy, &inc);          if (r)          return r;
        &copy, &rs->rs_notify_queue);
        spin_unlock_irqrestore(&rs->rs_lock, flags); }
        &copy, 1); }          return 0;
        &copy, PAGE_SIZE);
        &copy, __func__,          __LINE__);
        mpsslog("%s %s %d ",          mic->name, __func__,

```

```

        __LINE__);
0x%lx\n",
    }
    mpsslo
    &copy, __func__, __LINE__);
read failed %s ", mic->name, __func__, __LINE__,
    strerror(errno));
    mpsslog("cnt %d sum %zd\n",
    &copy, __func__, __LINE__);
read failed %s ", mic->name,
    __func__, __LINE__,
    strerror(errno));
    mpsslog("cnt %d sum %zd\n",
    &copy, argp, sizeof(copy))
    goto readfrom_err;
copy.loffset, copy.len, copy.roffset,
copy.flags);readfrom_err:
    &copy, argp, sizeof(copy))
    goto vreadfrom_err;
(void __force *)copy.addr, copy.len,
copy.roffset, copy.flags);vreadfrom_err:
    &copy, argp, sizeof(copy))
    goto vwriteto_err;
(void __force *)copy.addr, copy.len,
copy.roffset, copy.flags);vwriteto_err:
    &copy, argp, sizeof(copy))
    goto writeto_err;
copy.loffset, copy.len, copy.roffset,
copy.flags);writeto_err:
    &copy, argp, sizeof(copy))
    &copy->base); copy->clk = state->clk; copy->pclk =
state->pclk; copy->div = state->div; copy->planes =
state->planes;
    &copy->base); copy->tiling = state->tiling; copy->format =
state->format; copy->swap = state->swap;
    &copy->base;
    &copy->hc_hai.hai_fid),
copy->hc_data_version, data_version);
send error to cdt. Do not ask for
is modified frequently,
    &copy->hc_hai.hai_fid), rc,
copy->hc_hai.hai_cookie);
    /* hpk_errval must be >= 0 */
    hpk.hpk_errval = -rc;
    &copy->hc_hai.hai_fid);
    hpk.hpk_flags |= HP_FLAG_RETRY;
*/
PTR_ERR(inode);
    &copy->hc_hai.hai_fid);
    hpk.hpk_flags |= HP_FLAG_RETRY;
be >= 0 */
PTR_ERR(inode);
    &copy->state;
    &copy.out_len, sizeof(copy.out_len))
dev_err(mic_dev(mvdev), "%s %d errno ret %d\n",
    mpsslog("wrote to tap
len);#endif
    } else {
        mpsslog("%s %s %d
mic->name, __func__, __LINE__,
        strerror(errno));
        mpsslog("%s %s %d
__func__, __LINE__,
        mpsslog("cnt %d sum %zd\n",
        err = -EFAULT;
        err = scif_readfrom(priv,
        err = -EFAULT;
        err = scif_vreadfrom(priv,
        err = -EFAULT;
        err = scif_vwriteto(priv,
        err = -EFAULT;
        err = scif_writeto(priv,
        return -EFAULT;
        copy->pclk =
        copy->planes =
        copy->format =
        /* File was changed,
        * retry because if a file
        * the cdt wi
        hpk.hpk_flags |= HP_FLAG_RETRY;
        goto progress;
        if (IS_ERR(inode)) {
            /* hpk_errval is >= 0
            rc =
        if (IS_ERR(inode)) {
            /* hpk_errval must
            rc =
        }
    }

```



```

    __func__, __LINE__, -EFAULT);                return -EFAULT;
} break;
&copy; RGB (1998) Color Image Encoding Version 2005-05</title>
</biblioentry>
&copy;[1 req->saved_syn = copy; }
&copy;[i continue; } merge_bio.head =
copy[i].request->bio; merge_bio.tail = copy[i].request->biotail;
    bio_list_merge(&bio_list, &merge_bio);
copy[i].request->bio = NULL; blk_end_request
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free_filps if * the file happened to be read-only. 2) If the acc
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"LIST "zImage (RAM boot)"LIST "This script can be used to bo

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"hpi_internal.h"#include "hpidebug
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*****/#ifndef _HPIOS_H_#define _HPIOS_H_
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*****/#define SOURCEFILE_NAME "hpi6000.c"
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*****/#define SOURCEFILE_NAME "hpi6205.c"
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*****/#define SOURCEFILE_NAME "hpimsgx.c"#include
"hpi_internal.h"#include "hpi_version.
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*****//* Initialise response headers, or msg/response pairs.Note
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COPYRIGHT (CPU_CMD_MASK_CAPTURE
COPYRIGHT (0 << 2)
COPYRIGHT (0x1 << 2)
COPYRIGHT (1 << 2) #define HDMI_I2S_LINEAR_PCM (0
<< 1) #define HDMI_I2S_NO_LINEAR_PCM (1 << 1) #define
HDMI_I2S_CONSUMER_FORMAT (0) #define HDMI_I2S_PROF_FORMAT
(1) #define HDMI_I2S_CH_ST_0_CLR
COPYRIGHT (1 << 4) #define AC_DIG1_NONAUDIO
(1 << 5) #define AC_DIG1_PROFESSIONAL (1 << 6) #define AC_DIG1_LEVEL
(1 << 7)
COPYRIGHT 0x00000004 /
COPYRIGHT 0xd4 #define CX2341X_ENC_SET_EVENT_NOTIFICATION
0xd5 #define CX2341X_ENC_SET_NUM_VSYNC_LINES 0xd6 #define
CX2341X_ENC_SET_PLACEHOLDER 0xd7 #define CX2341X_ENC_MUTE_VIDEO
0xd9 #define
COPYRIGHT (0) #define EEPROM_COPYRIGHT_LEN (44) #define
EEPROM_CHECKSUM (62) #define EEPROM_CHECKSUM_REV (63) #define
EEPROM_MAC (64) #define EEPROM_MAC_REV (70) #define
EEPROM_SERIAL (112) #defin
COPYRIGHT (1 << 2) / #define IEC958_AES0_CON_EMPHASIS (7 << 3)
/* mask - emphasis */ #define IEC958_AES0_CON_EMPHASIS_NONE
(0 << 3) /* none emphasis */ #define IEC958_AES0_CON_EMPHASIS_5015
(1 << 3) /* 50/15u
COPYRIGHT (IEC958_AES1_CON_ADC_COPYRIGHT_ID #define
IEC958_AES1_CON_ADC_COPYRIGHT_OTHER

```

(IEC958_AES1_CON_ADC_COPYRIGHT_ID|0x60)#define
IEC958_AES1_CON_SOLIDMEM_MASK      0x0f#define IEC958_AES1_CON_SOLID
    COPYRIGHT      0#define DVD_CPM_COPYRIGHTED 1
    COPYRIGHT      0x01#define DVD_STRUCT_DISCKEY      0x02#define
DVD_STRUCT_BCA      0x03#define DVD_STRUCT_MANUFACT      0x04
    COPYRIGHT      | HDMI_I2S_LINEAR_PCM      |
HDMI_I2S_CONSUMER_FORMAT); hdmi_reg_writeb(hdata, HDMI_I2S_CH_ST_1,
HDMI_I2S_CD_PLAYER); hdmi_reg_writeb(hdata, HDMI_I2S_CH_ST_2,
HDMI_I2S_SET_SOURCE_NUM(0));
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    COPYRIGHT=====
    COPYRIGHT=====
=====The exofs file system is based on ext2 v0.5b
(distributed with the Linux kernel version 2.6.10). All files in
    COPYRIGHTEnum      212/0xD4Description
    COPYRIGHT      = 0x10,      AUDIO_STATUS_NONAUDIO      = 0x20,
    AUDIO_STATUS_PROFESSIONAL = 0x40,      AUDIO_STATUS_LEVEL      =
0x80};
    COPYRIGHT &&      i
    COPYRIGHT (C) 2001-2004 by Agere Systems Inc. All Rights Reserved
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    COPYRIGHT (C) by L. Wassmann <LW@KARO-electronics.de> */
    COPYRIGHT (c) 2008 * The Regents of the University of Michigan *
ALL RIGHTS RESERVED
    COPYRIGHT );#endif
    COPYRIGHT + EEPROM_COPYRIGHT_LEN      DPRINTK("eeprom: copyright
= \"%s\\\"\\n\",      (char *) &e[EEPROM_COPYRIGHT]);      else
    COPYRIGHT + EEPROM_COPYRIGHT_LEN); i      if (e[i] < 0x20 ||
e[i] > 0x7E)      break;
    COPYRIGHT 0xe3      /
    COPYRIGHT |
IEC958_AES0_CON_EMPHASIS;      ucontrol->value.iec958.status[1] =
IEC958_AES1_CON_ORIGINAL |
IEC958_AES1_CON_CATEGORY;      ucontrol->value.iec958.status[3] =
IEC958_AES3_C
    COPYRIGHT |
IEC958_AES0_CON_EMPHASIS;      ucontrol->value.iec958.status[1] =
IEC958_AES1_CON_ORIGINAL |
IEC958_AES1_CON_CATEGORY;      ucontrol->value.iec958.status[3] =
IEC958_AES3_CON
    COPYRIGHT |      IEC958_AES0_CON_EMPHASIS_5015;
ctrl->ch_status[1] = IEC958_AES1_CON_DIGDIGCONV_ID;
ctrl->ch_status[2] = 0x00; ctrl->ch_status[3] =
IEC958_AES3_CON_FS_44100 |      IEC958_AES3
    COPYRIGHT | IEC958_AES0_CON_EMPHASIS;
value->value.iec958.status[1] =      IEC958_AES1_CON_CATEGORY |
IEC958_AES1_CON_ORIGINAL;      return 0;}

```



```

    COPYRIGHT | IEC958_AES0_CON_EMPHASIS_NONE;    cs[1] =
IEC958_AES1_CON_GENERAL;    cs[2] = IEC958_AES2_CON_SOURCE_UNSPEC |
IEC958_AES2_CON_CHANNEL_UNSPEC;    cs[3] = IEC958_AES3_CON_CLOCK_1000PPM |
fs;

    COPYRIGHT | SPCS_NOTAUDIODATA;
    snd_emul0k1_ptr_write(emu, SPCS0 + i, 0, bits);    }
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    COPYRIGHT)    bits    else    bits |= 0x08;
        switch (diga->status[0] & IEC958_AES0_CON_EMPHASIS) {
    default:
    COPYRIGHT)    snd_iprintf(buffer, " Non-Copyright"); if (digil
& AC_DIG1_NONAUDIO)    snd_iprintf(buffer, " Non-Audio"); if (digil
& AC_DIG1_PROFESSIONAL)    snd_iprintf(buffer, " Pro"); if (digil
& A
    COPYRIGHT) >> 2; REG_FLD_MOD(base, HDMI_CORE_FC_AUDSCHNLS(0), val,
0, 0);
    COPYRIGHT) ? 0 : AC97_CXR_COPYRGT;    v |= new &
IEC958_AES0_NONAUDIO ? AC97_CXR_SPDIF_AC3 : AC97_CXR_SPDIF_PCM;
    change |= snd_ac97_update_bits_nolock(ac97, AC97_CXR_AUDIO_MISC,
AC97_CXR
    COPYRIGHT) \    | ((IEC958_AES1_CON_MIXER \
| IEC958_AES1_CON_ORIGINAL) << 8) \    |
(0x10 << 16) \    | ((IEC958_AES3_CON_FS_48000) << 24))
    COPYRIGHT) {    snd_iprintf(buffer, "permitted\n");
        } else {    snd_iprintf(buffer, "protected\n");
        }    snd_iprintf(buffer, "Emphasis: ");    if
((status[0] & IEC958_AES0_CON_EMPHASIS) != IEC958_AE
    COPYRIGHT))    bits    if ((diga->status[0] &
IEC958_AES0_CON_EMPHASIS) == IEC958_AES0_CON_EMPHASIS_NONE)
    bits |= 0x40;    if (!(diga->status[1] &
IEC958_AES1_CON_ORIGINAL))    bits |= 0x80;    if ((d
    COPYRIGHT))    sbits    if (val & AC_DIG1_LEVEL)
    sbits |= (IEC958_AES1_CON_ORIGINAL << 8);    sbits |=
val & (0x7f << 8);    }    return sbits;
    COPYRIGHT)    val    if (sbits &
(IEC958_AES1_CON_ORIGINAL << 8))    val |= AC_DIG1_LEVEL;
    val |= sbits & (IEC958_AES1_CON_CATEGORY << 8);    }    return
val;

    COPYRIGHT))    val    val |= ((new >> 8) & 0xff) << 4;
    // category + original    switch ((new >> 24) & 0xff) {
    case IEC958_AES3_CON_FS_44100: val |= 0<<12; break;    case
IEC958_AES3_CON_FS_48000: val
    COPYRIGHT)) {    printk("
    COPYRIGHT);    DBG(("failed.\n"));    return FALSE;    }
    COPYRIGHT);    new    new |=
((ucontrol->value.iec958.status[3] & IEC958_AES3_CON_FS) << 24);
    if ((new & IEC958_AES0_CON_EMPHASIS) ==
IEC958_AES0_CON_EMPHASIS_5015)    val |= 1<<3;
    COPYRIGHT); snd_emul0k1x_ptr_write(chip, SPCS1, 0,
chip->spdif_bits[1] =    SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 |    SPCS_CHANNELNUM_LEFT |
SPCS_SOURCENUM_UNSPEC |

```

```

        COPYRIGHT); snd_emul0k1x_ptr_write(chip, SPCS2, 0,
chip->spdif_bits[2] = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT |
SPCS_SOURCENUM_UNSPEC |
        COPYRIGHT); } return pci_register_driver({
        COPYRIGHT);MODULE_LICENSE("GPL");
        COPYRIGHT);MODULE_LICENSE("GPL");
        COPYRIGHT);MODULE_LICENSE("GPL");MODULE_ALIAS_MISCDEV(TUN_MINOR);
        COPYRIGHT);MODULE_LICENSE("GPL");MODULE_FIRMWARE("ipw2200-
ibss.fw");#ifdef CONFIG_IPW2200_MONITORMODULE_FIRMWARE("ipw2200-
sniffer.fw");#endifMODULE_FIRMWARE("ipw2200-bss.fw");
        COPYRIGHT);MODULE_LICENSE("GPL");MODULE_VERSION(DRV_VERSION);MODULE
_FIRMWARE(FIRMWARE_D101M);MODULE_FIRMWARE(FIRMWARE_D101S);MODULE_FIRMWARE
(FIRMWARE_D102E);
        COPYRIGHT: return "SET_COPYRIGHT"; case
CX2341X_ENC_SET_EVENT_NOTIFICATION:
        COPYRIGHT: return "SET_COPYRIGHT"; case
CX2341X_ENC_SET_EVENT_NOTIFICATION:
        COPYRIGHT: return dvd_read_copyright(cdi, s, cgc);
        COPYRIGHT; switch (bits case 0x10: diga->status[0]
|= IEC958_AES0_CON_EMPHASIS_NONE; break; case 0x00:
diga->status[0] |= IEC958_AES0_CON_EMPHASIS_5015; break; }
        if (!(bits & 0x80)) diga-
        COPYRIGHT; buf buf[2] = IEC958_AES3_CON_FS_NOTID;buf[3] =
IEC958_AES4_CON_ORIGFS_NOTID |
        IEC958_AES4_CON_MAX_WORDLEN_24; reg_write_range(priv,
REG_CH_STAT_B(0), buf, 4);
        COPYRIGHT; cs_bytes
        COPYRIGHT; regmap_write(ak4104->regmap, AK4104_REG_CHN_STATUS(0),
val);
        COPYRIGHT; ucontrol->value.iec958.status
        IEC958_AES1_CON_ORIGINAL; return 0;}
        COPYRIGHT; ucontrol->value.iec958.status
        IEC958_AES1_CON_ORIGINAL; ucontrol->value.iec958.status[3] =
IEC958_AES3_CON_FS; return 0;}
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Public License as published by the Free Software Foundation; vers
        Copyright */

```

```

Copyright          : %s\n", (status          snd_iprntf(buffer,
"Emphasis          : %s\n", emphasis[(status & SPCS_EMPHASISMASK) >>
3]);          snd_iprntf(buffer, "Mode          : %i\n", (status
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 _ASM_FPREGDEF_H#define _ASM_FPREGDEF_H
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 the following chars caused a compiler warning on HIUX (so I re
 Copyright (C) 1990, 1999 by Silicon Graphics, Inc. */#ifndef
 _ASM_ADDRSPACE_H#define _ASM_ADDRSPACE_H
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 _ASM_AR7_SPACES_H#define _ASM_AR7_SPACES_H
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 _ASM_MACH_IP22_SPACES_H#define _ASM_MACH_IP22_SPACES_H
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 _ASM_TX39XX_SPACES_H#define _ASM_TX39XX SPACES_H
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* Jan-11-1998, C. Scott Ananian <cananian@alumni.princeton.edu> *
Shared /dev/zero mmappping support, Feb 2000, Kanoj Sarcar <
Copyright (C) 1991, 1992 Linus Torvalds * * Added support for a
Unix98-style ptmx device. * -- C. Scott Ananian
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endian byte-swapping/bitmaps by * David S. Miller
(davem@caip.rutgers.edu), 1995 * Directory entry file type support and
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detection and feature setting code by Mike Jagdis * and Martin Mares,
November 1997.
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to support mounting root fs via NFS * Added initrd & change_root: Werner
Almesberger & Hans Lermen, Feb '96 * Moan early if gc
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<linux/fs.h>#include <linux/ext2_fs.h>#include
<linux/blockgroup_lock.h>#include <linux/percpu_counter.h>#include
<linux/rbtree.h>

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 Russell King * * We now have independent partition support from the *
 block drivers, which allows all the partition code to * b
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 Russell King */
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 Copyright (C) 1992 Doug Evans * * coh/alloc.c
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 2 disks attached. Do a lot of checking * to make sure that we are not
 getting blocks mixed up, and PANIC if * anything out of t
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 handling functions
 Copyright (C) 1992 Rick Sladkey * * nfs superblock handling
 functions
 Copyright (C) 1992 Rick Sladkey * * structure passed from user-
 space to kernel-space during an nfs mount
 Copyright (C) 1992 Rick Sladkey */#include <linux/fs.h>#include
 <linux/file.h>#include <linux/falloc.h>#include <linux/nfs_fs.h>#include
 <uapi/linux/btrfs.h> /* BTRFS_IOC_CLONE/BTRFS_IOC_CLONE_
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Senn * * Fixes:
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__ASM_SN_NMI_H#define __ASM_SN_NMI_H
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    Copyright (C) 1992 - 1997, 2000 Silicon Graphics, Inc. * * gda.h --
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suser() last after "normal" permission checks so we * get BSD
style process accounting right. * Occurs in sever

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"ip:%c, ep:%c, pp:%c.\n", driver_name, tag_mode,
YESNO(linked_comm
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options -> of:%c, tm:%d, lc:%c, mq:%d, et:%c.\n",
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Rework bus reset code for new error handler *

Add delay after initial bus reset *

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<linux/ds1286.h>#include <linux/module.h>#include
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<linux/clockchips.h>#include <linux/clocksource.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/sched_cloc

```

    Copyright (C) 1998 Phil Blundell */#include
<linux/clockchips.h>#include <linux/i8253.h>#include
<linux/init.h>#include <linux/interrupt.h>#include <linux/irq.h>#include
<linux/spinlock.h>#inc
    Copyright (C) 1998 Phil Blundell */#include
<linux/ioport.h>#include <linux/init.h>#include <linux/dma-
mapping.h>#include <linux/io.h>
    Copyright (C) 1998 Philip Blundell <philb@gnu.org>
    Copyright (C) 1998 Philip Blundell <philb@gnu.org> * * Do the
dirty work of rebooting the machine. Basically we need to undo all the *
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contains the HP300-specific initialisation code. It gets * called by
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contains the HP300-specific time handling code.
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```

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 <linux/screen_info.h>#include <linux/io.h>#include <linu
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 <linux/module.h>#include <linux/ioport.h>#include
 <linux/kernel.h>#include <linux/delay.h>#include <linux/init.h>#include
 <linux/
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__ASM_ARCH_DMA_H#define __ASM_ARCH_DMA_H
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S.Eranian added ia64_set_iva() * 12/03/99 D. Mosberger

implement thread_saved_pc() via kernel unwind API * 06/16/00 A.

Mallick add

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Copyright (C) 1999 Nicolas Pitre (full GPIO irq isolation) * * 2001/11/14 RMK Cleaned up and standardised a lot of the IRQs.

Copyright (C) 1999 Nicolas Pitre <nico@fluxnic.net> * * SA1100 specific tweaks. This is merged into head.S by the linker.

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_ASM_PCI_BRIDGE_H#define _ASM_PCI_BRIDGE_H
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_ASM_SN_ARCH_H#define _ASM_SN_ARCH_H
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_ASM_SN_SN0_ARCH_H#define _ASM_SN_SN0_ARCH_H
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_ASM_XTALK_XTALK_H#define _ASM_XTALK_XTALK_H
Copyright (C) 1999 Ralf Baechle (ralf@gnu.org) */#ifndef
_ASM_XTALK_XWIDGET_H#define _ASM_XTALK_XWIDGET_H
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Shimoda <yoshihiro.shimoda.uh@renesas.com>
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and SOF generation) as well as peripheral side USB * (like t
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_ASM_MMU_CONTEXT_H#define _ASM_MMU_CONTEXT_H

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_ASM_SGIARCS_H#define _ASM_SGIARCS_H

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_ASM_SIGCONTEXT_H#define _ASM_SIGCONTEXT_H

Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef

_ASM_SIGNAL_H#define _ASM_SIGNAL_H

Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef

_ASM_TYPES_H#define _ASM_TYPES_H

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_UAPI_ASM_SIGCONTEXT_H#define _UAPI_ASM_SIGCONTEXT_H

Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef

_UAPI_ASM_SIGNAL_H#define _UAPI_ASM_SIGNAL_H

Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef

_UAPI_ASM_TYPES_H#define _UAPI_ASM_TYPES_H

Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef

_ASM_BREAK_H#define _ASM_BREAK_H

Copyright (C) 1999 Silicon Graphics, Inc. */#ifndef

_UAPI_ASM_BREAK_H#define _UAPI_ASM_BREAK_H

Copyright (C) 1999 Silicon Graphics, Inc. */#include

<asm/mipsregs.h>#include <asm/regdef.h>#include <asm/stackframe.h>

Copyright (C) 1999 Silicon Graphics, Inc. */#include

<linux/compiler.h>#include <linux/init.h>#include

<linux/kernel.h>#include <linux/irqflags.h>

Copyright (C) 1999 Silicon Graphics, Inc. */#include

<linux/init.h>#include <asm/fw/arc/types.h>#include <asm/sgialib.h>

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<sprasad@sprasad.engr.sgi.com> * * 02/01/04 J. Hall Updated Error Record

Structures to conform to July 2001 * revision of the SAL

spec. * 01/

```

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on skeleton from the drivers/char/rtc.c driver by P. Gortmaker
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_LINUX_RTC_H#define _LINUX_RTC_H_
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_UAPI_LINUX_RTC_H#define _UAPI_LINUX_RTC_H_
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1994-07-02 Alan Modra * fixed set_rtc_mmss, fixed time.year
for >= 2000, new mktime * 1998-12-20 Updated NTP code ac
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_FS_BFS_H#define _FS_BFS_H
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Made nbd_end_request() use the io_request_lock
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_IA64_PLATFORM_HPSIM_SSC_H#define _IA64_PLATFORM_HPSIM_SSC_H
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<linux/console.h>#include <linux/init.h>#include <linux/kdev_t.h>#include
<linux/kernel.h>#include <linux/major.h>#include <linu
    Copyright (C) 1999 Walt Drummond <drummond@valinux.com>
    Copyright (C) 1999 Walt Drummond <drummond@valinux.com> * *
12/26/04 S.Siddha, G.Jin, R.Seth * Add multi-threading and
multi-core detection * 11/12/01 D.Mosberger Convert get_cpuinfo() to
seq_fi
    Copyright (C) 1999 Walt Drummond <drummond@valinux.com> * * This
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spaces of the SH-4 CPUs.
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_ASM_IA64_SN_KLCONFIG_H#define _ASM_IA64_SN_KLCONFIG_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_IA64_SN_TYPES_H#define _ASM_IA64_SN_TYPES_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SGI_SN0_HUBNI_H#define _ASM_SGI_SN0_HUBNI_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SGI_SN_SN0_HUBIO_H#define _ASM_SGI_SN_SN0_HUBIO_H
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_ASM_SN_SN0_ADDRS_H#define _ASM_SN_SN0_ADDRS_H
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_ASM_SN_SN0_HUBMD_H#define _ASM_SN_SN0_HUBMD_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SN_SN0_HUBPI_H#define _ASM_SN_SN0_HUBPI_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef
_ASM_SN_SN0_HUB_H#define _ASM_SN_SN0_HUB_H
    Copyright (C) 1999 by Ralf Baechle */#ifndef _ASM_SN_TYPES_H#define
_ASM_SN_TYPES_H
    Copyright (C) 1999 by Silicon Graphics, Inc.
    Copyright (C) 1999 by Silicon Graphics, Inc. */#include
<linux/kernel.h>#include <linux/mm.h>
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file "COPYING" in the main directory of this archive * for m
    Copyright (C) 1999, 2000 Kaz Kojima * Some code taken from arm
version:
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    Copyright (C) 1999, 2000 Niibe Yutaka * *      In turn started from
i386 version:
    Copyright (C) 1999, 2000 Niibe Yutaka * * Simple interrupt
handling using IMASK of SR register.
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    Copyright (C) 1999, 2000 Niibe Yutaka */#ifndef
_UAPI__ASM_SH_PTRACE_H#define _UAPI__ASM_SH_PTRACE_H

```

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    Copyright (C) 1999, 2000 Ralf Baechle (ralf@gnu.org) */#ifndef
__ASM_SGI_SN_AGENT_H#define __ASM_SGI_SN_AGENT_H
    Copyright (C) 1999, 2000 Silicon Graphics, Inc.
    Copyright (C) 1999, 2000 Silicon Graphics, Inc.
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. * * Changed system
calls macros _syscall5 - _syscall7 to push args 5 to 7 onto
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    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef
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__ASM_POSIX_TYPES_H#define __ASM_POSIX_TYPES_H
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef
__ASM_PROCESSOR_H#define __ASM_PROCESSOR_H
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef
__ASM_PTRACE_H#define __ASM_PTRACE_H
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef
__ASM_SIM_H#define __ASM_SIM_H

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_ASM_SPINLOCK_H#define _ASM_SPINLOCK_H
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_IOC3_H#define _IOC3_H
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#ifndef
_UAPI_ASM_PTRACE_H#define _UAPI_ASM_PTRACE_H
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include
<linux/cache.h>#include <linux/compat.h>#include <linux/sched.h>#include
<linux/mm.h>#include <linux/smp.h>#include <linux/kernel.h>#
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include
<linux/compiler.h>#include <linux/kernel.h>#include
<linux/sched.h>#include <linux/timer.h>#include <linux/smp.h>#include
<linux/mmzon
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include
<linux/cpu_pm.h>#include <linux/hardirq.h>#include <linux/init.h>#include
<linux/highmem.h>#include <linux/kernel.h>#include <linux/li
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include
<linux/kernel.h>#include <linux/export.h>#include <linux/pci.h>#include
<linux/smp.h>#include <asm/sn/arch.h>#include <asm/pci/bridge.
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include
<linux/kernel.h>#include <linux/sched.h>#include
<linux/interrupt.h>#include <linux/kernel_stat.h>#include
<linux/param.h>#include <li
    Copyright (C) 1999, 2000 Silicon Graphics, Inc. */#include
<linux/pci.h>#include <asm/paccess.h>#include <asm/pci/bridge.h>#include
<asm/sn/arch.h>#include <asm/sn/intr.h>#include <asm/sn/sn0/h
    Copyright (C) 1999, 2000 by Ralf Baechle */#ifndef
_ASM_SN_ADDRS_H#define _ASM_SN_ADDRS_H
    Copyright (C) 1999, 2000 by Ralf Baechle */#ifndef
_ASM_SN_KLCONFIG_H#define _ASM_SN_KLCONFIG_H
    Copyright (C) 1999, 2000 by Ralf Baechle */#ifndef
_ASM_SN_KLDIR_H#define _ASM_SN_KLDIR_H
    Copyright (C) 1999, 2000 by Silicon Graphics
    Copyright (C) 1999, 2000 by Silicon Graphics, Inc. * * PROM library
functions for acquiring/using memory descriptors given to us * from the
ARCS firmware. This is only used when CONFIG_ARC_MEMORY
    Copyright (C) 1999, 2000, 01 Silicon Graphics, Inc. */#include
<linux/interrupt.h>#include <linux/export.h>#include
<asm/checksum.h>#include <linux/mm.h>#include <asm/uaccess.h>#include
<asm/ft
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Creasey (sammy@sammy.net)
    Copyright (C) 1999, 2000, 2001 Axis Communications AB * * 1994-07-
02 Alan Modra * fixed set_rtc_mmss, fixed time.year for >= 2000,
new mktime * 1995-03-26 Markus Kuhn * fixed 500 ms bu
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_ASM_CACHEFLUSH_H#define _ASM_CACHEFLUSH_H
    Copyright (C) 1999, 2000, 2001 Silicon Graphics, Inc. */#ifndef
_ASM_PGALLOC_H#define _ASM_PGALLOC_H
    Copyright (C) 1999, 2000, 2001 Silicon Graphics, Inc. */#ifndef
_ASM_PGTABLE_32_H#define _ASM_PGTABLE_32_H
    Copyright (C) 1999, 2000, 2001 Silicon Graphics, Inc. */#ifndef
_ASM_PGTABLE_64_H#define _ASM_PGTABLE_64_H
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All rights reserved
    Copyright (C) 1999, 2001 Axis Communications AB * * Code that sets
up the DRAM registers, calls the * decompressor to unpack the
piggybacked kernel, and jumps.
    Copyright (C) 1999, 2001 David Mosberger-Tang <davidm@hpl.hp.com>
*/
    Copyright (C) 1999, 2001 Hewlett-Packard Co
    Copyright (C) 1999, 2001 Hewlett-Packard Co * Stephane Eranian
<eranian@hpl.hp.com> * David Mosberger <davidm@hpl.hp.com> * * 4/06/01
davidm      Tuned to make it perform well both for cached and uncac
    Copyright (C) 1999, 2001 Hewlett-Packard Co * Stephane Eranian
<eranian@hpl.hp.com> * * 09/24/99 S.Eranian add speculation recovery code
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    Copyright (C) 1999, 2001 Silicon Graphics, Inc. * * Heavily
inspired by the 32-bit Sparc compat code which is
    Copyright (C) 1999, 2001 Silicon Graphics, Inc. */#include
<asm/asm.h>#include <asm/asmmacro.h>#include <asm/errno.h>#include
<asm/fpregdef.h>#include <asm/mipsregs.h>#include <asm/asm-offsets.

```



```

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<linux/mman.h>
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 <linux/kernel.h>#include <linux/pci.h>#include <linux/types.h>
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 05/22/2000 eranian Added support for stacked register calls * 05
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<asm/page.h>#include <asm/processor.h>#include <asm/sn/a

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<linux/kernel.h>#include <linux/init.h>#include <linux/sched.h>#include
<linux/smp.h>#include <linux/mm.h>#include <linux/module.h

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file implements mcount(), which is used to collect profiling data. * This
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<linux/interrupt.h>#include <linux/ioport.h>#include <linux/irq.h>#incl

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<linux/export.h>#include <linux/screen_info.h>#include
<linux/memblock.h>#incl

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<linux/interrupt.h>#include <linux/smp.h>#include <linux/sp

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<linux/bootmem.h>#include <linux/types.h>

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<linux/init.h>#include <linux/kernel_stat.h>#include

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<linux/errno.h>#include <asm/asm.h>#include <asm/asmmacro.h>#include
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__ASM_PGTABLE_BITS_H#define __ASM_PGTABLE_BITS_H
    Copyright (C) 2002 Maciej W. Rozycki */#include
<linux/init.h>#include <linux/kernel.h>#include <linux/module.h>#include
<linux/signal.h> /* for SIGBUS */#include <linux/sched.h> /*
schow_regs(
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<linux/init.h>#include <linux/kernel.h>#include <linux/sched.h>#include
<asm/traps.h>#include <asm/uaccess.h>#include <asm/addrspace.h>#include
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<linux/kernel.h>#include <linux/sched.h>#include <linux/smp.h>#include
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<linux/init.h>#include <linux/initrd.h>#include <linux/bootmem.h>#include
<linux/console.h>

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<linux/module.h>#include <linux/kernel.h>#include <linux/errno.h>#include
<linux/delay.h>#include <linux/tty.h>#include <linux/t
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<linux/elevator.h>#include <linux/bio.h>#include <linux/module.
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McKinley * 3/08/02 davidm Some more tweaking
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<asm/asmmacro.h>#include <asm/page.h>
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 <linux/in.h>#include <linux/kernel.h>#include <linux/ji
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__ASM_MACH_GENERIC_IRQ_H#define __ASM_MACH_GENERIC_IRQ_H
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Copyright (C) 2003 by Ralf Baechle */#include
<linux/init.h>#include <linux/mm.h>#include <asm/fixmap.h>#include
<asm/pgtable.h>#include <asm/pgalloc.h>#include <asm/tlbflush.h>
Copyright (C) 2003 by Ralf Baechle */#include
<linux/init.h>#include <linux/mm.h>#include <linux/bootmem.h>#include
<linux/highmem.h>#include <asm/fixmap.h>#include <asm/pgtable.h>#include
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int val, int len);
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<linux/stacktrace.h>#include <linux/writeback.h>#include
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*/#include <linux/rwsem.h>#include <linux/mutex.h>#include <linux/ww_mutex.h>#include <linux/sched.h>#include <linux/delay.h>#in

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*/#include <linux/sched.h>#include <linux/kernel.h>#include <linux/export.h>#include <linux/kallsyms.h>#include <linux/stacktrace

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<linux/types.h>#include <linux/init.h>#include <linux/sched.h>#include <linux/interrupt.h>#include <linux/irq.h>#include <linux/kallsym

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<linux/init.h>#include <linux/export.h>#include <linux/console.h>#include
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<linux/serial_8250.h>#include <linux/rtc/ds1685.h>
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<linux/percpu.h>#include <linux/smp.h>#include <linux/irq.h>
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<linux/mtd/map.h>#include <linux/of_p
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<linux/pci.h>#include <linux/interrupt.h>#include <linux/time.h>#include
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davidm Make sure we pass zero for reserved parameters. *
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<linux/spinlock.h>#include <linux/irqflags.h>#include <linux/uaccess.h>#

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<linux/trace_clock.h>#include <linux/trace_seq.h>#include <linux/s

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<linux/module.h>#include <linux/slab.h>#include <linux/cpu.h>#include  
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"event.h"#include "symbol.h"#include <linux/kernel.h>#inc

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Cortex-A9 with per-CPU * Local timer and Watchdog, GIC, SCU, PL

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<linux/kernel.h>#include <linux/mm.h>#include <linux/bootmem.h>#include

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 <linux/mm.h>#include <linux/module.h>#include <linux/proc_fs.h>

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__ASM_RTLX_H#define __ASM_RTLX_H
    Copyright (C) 2013 Imagination Technologies Ltd. */#include
<linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include
<linux/wait.h>#include <linux/sched.h>#include <linux/interrupt.h>
    Copyright (C) 2013 Imagination Technologies Ltd. */#include
<linux/device.h>#include <linux/fs.h>#include <linux/err.h>#include
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    Copyright (C) 2013 Imagination Technologies Ltd. */#include
<linux/init.h>#include <linux/libfdt.h>#include
<linux/of_platform.h>#include <linux/of_fdt.h>
    Copyright (C) 2013 Imagination Technologies Ltd. */#include
<linux/kernel.h>#include <linux/device.h>#include <linux/fs.h>#include
<linux/slab.h>#include <linux/export.h>
    Copyright (C) 2013 Imagination Technologies Ltd. */#include
<linux/kernel.h>#include <linux/fs.h>#include <linux/syscalls.h>#include
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<linux/bug.h>#include <linux/kernel.h>#include <linux/math64.h>#include  
<linux/export.h>#include <linux/ctype.h>#include <linux/errno.h>#includ

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<linux/in.h>#include <linux/kernel.h>#include <linux/mod  
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Copyright (C) Riccardo Facchetti 1995-98\n"); if (io == -1 || dma == -1 || irq == -1) { printk(KERN\_INFO "aedsp16: I/O, IRQ and DMA are mandatory\n"); return -EINVAL; }

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 Copyright (c) 1997 Dan Malek (dmalek@jlc.net). \*/#define BD\_SC\_EMPTY ((ushort)0x8000) /\* Receive is empty \*/#define BD\_SC\_READY ((ushort)0x8000) /\* Transmit is ready \*/#define BD\_SC\_WRAP ((ushort)  
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        "NCR D700:\n");      banner = 0; }      /* now do the bus
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        base_a
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"Initializing module"); RT\_TRACE(COMP\_INIT, "Wireless extensions

version %d", WIRELESS\_EXT); rtl8192\_proc\_module\_init(); return usb\_r

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<linux/nfs.h>#include <linux/nfs3.h>#include <linux/nfs4.h>#include
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"xfs_format.h"#include "xfs_log_format.h"#include
"xfs_trans_resv.h"#include "xfs_sb.h"#include "xfs_mount.h"#include
"xfs_inod
 Copyright (c) 2014 Christoph Hellwig. */#include
<linux/exportfs.h>#include <linux/genhd.h>#include <linux/slab.h>
 Copyright (c) 2014 Christoph Hellwig. */#include
<linux/kmod.h>#include <linux/file.h>#include <linux/jhash.h>#include
<linux/sched.h>#include <linux/sunrpc/addr.h>
 Copyright (c) 2014 Christoph Hellwig. */#include
<linux/sunrpc/svc.h>#include <linux/blkdev.h>#include
<linux/nfs4.h>#include <linux/nfs_fs.h>#include <linux/nfs_xdr.h>
 Copyright (c) 2014 Christoph Hellwig. */#include
<linux/sunrpc/svc.h>#include <linux/exportfs.h>#include <linux/nfs4.h>
 Copyright (c) 2014 Christoph Hellwig. */#undef TRACE_SYSTEM#define
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21:24:07 1998 by root@home

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<linux/fs.h>#include <linux/list.h>#include <linux/highmem.h>#include  
<linux/compiler.h>#include <asm/uaccess.h>#include <linux/gf

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<linux/ioport.h>#include <linux/delay.h>#include <linux/blkdev.h>#include

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Firmware Version: 4.07-0-07, Channels: 1, Memory Size: 16MB PCI Bus: 1,  
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return double(temp)\*0.427-32.08; \*else if(temp>=169 && temp<=202) \*  
return double(temp)\*0.582-58.16; \*else \* return  
double(temp)\*  
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voltagefactor[0]=1.25/2628; (2628/1.25=2102.4) // Vccp \*  
voltagefactor[1]=1.25/2628; (2628/1.25=2102.4) // +2.5V \*  
voltagefactor[2]=1.67/2628; (262  
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<drm/drm_plane_helper.h>
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<linux/etherdevice.h>#include <linux/slab.h>#include  
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 <linux/kernel.h>#include <linux/interrupt.h>#include <linux/fs.h>#include  
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<linux/in.h>#include <linux/kernel.h>#include <linux/timer.h>
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<linux/types.h>#include <linux/slab.h>#include <linux/socket.h>#include
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(cards w/ SPI DAC) * * This code was initially based on code fr
Copyright message. * 0.0.19 * Added I2C and SPI registers.
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24bit. * 0.0.21 * Implement support for Line-in capture o
Copyright message. * 0.0.19 * Finally fix support for SB Live
24 bit. SB0410 and SB0413. * The output codec needs resetting,
otherwise all output is muted. * 0.0.20 * Merge "pci_disabl
Copyright so I * taken from PPCBoot (now U-Boot). Also based on
drivers/serial/8250.c * by Russell King. *
Copyright status#define AC97_ALC650_PRE 0x0038 /*
Preemphasis status */#define AC97_ALC650_PRE_SHIFT 3#define
AC97_ALC650_MODE 0x00C0 /* Preemphasis status */#define
Copyright status#define AC97_SC_PRE 0x0008 /*
Preemphasis status */#define AC97_SC_CC_MASK 0x07f0 /*

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Category Code mask */#define AC97_SC_CC_SHIFT 4#define AC97_SC_L
0x0800 /* Generation Level
Copyright string). This works with any SB and compatible. -
Added the code to request_region at device init (should go in the main
body of voxware). v0.4 (BETA) - Better configure.c pat
Copyright unasserted) * AN = 0 (Audio data)
* P = 0 (Consumer) */ def_bits =
SPCS_CLKACCY_1000PPM | SPCS_SAMPLERATE_48 |
SPCS_CHANNELNUM_LEFT | SPCS
Copyright unasserted) * AN = 0 (Audio data)
* P = 0 (Consumer) */
emu->spdif_bits[0] = emu->spdif_bits[1] =
emu->spdif_bits[2] = SPCS_CLKACCY_1000PPM
Copyright unasserted) * AN = 0 (Audio data)
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snd_emul0klx_ptr_write(chip, SPCS0, 0,
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and conditions of the GNU General Public License, * versio
 Copyright(c) 1999 - 2015 Intel Corporation.\n"); ret =
pci_register_driver(&el000_driver);
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*****/#ifndef VXGE_CONFIG_H#define VXGE_CONFIG_H#include
<linux/hardirq.h>#include <linu
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*****/#ifndef VXGE_MAIN_H#define VXGE_MAIN_H

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*****/#ifndef VXGE_TRAFFIC_H#define VXGE_TRAFFIC_H
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*****/#ifndef VXGE_VERSION_H#define VXGE_VERSION_H
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*****/#ifndef _VXGE_ETHTOOL_H#define _VXGE_ETHTOOL_H
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*****/#include <linux/etherdevice.h>#include <linux/prefetch.h>
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*****/#include <linux/ethtool.h>#include <linux/slab.h>#include
<linux/pci.h>#include <l
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*****/#include <linux/vmalloc.h>#include <linux/etherdevice.h>#include
<linux/pci.h>#inc
 Copyright(c) 2002-2010 Exar Corp.\n"); DBG_PRINT(ERR_DBG, "%s:
Neterion %s (rev %d)\n", dev->name, sp->product_name,
pdev->revision); DBG_PRINT(ERR_DBG, "%s: Driver version %s\n", dev->name,

 Copyright(c) 2002-2010 Exar Corp.\n"); pr_info("Driver
version: %s\n", DRV_VERSION);
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"<ilw@linux.intel.com>"
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 Copyright(c) 2003-2006 Intel Corporation"#define DRV_VERSION
IPW2200_VERSION

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Copyright:\n"));

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Copyright; struct regid appId; struct timestamp
recordingDateAndTime; struct regid impId; uint8_t
 impUse[64]; __le32
predecessorVolDescSeqLocation; __le16 flags;
uint8_t reserve
copyright for type == DVD_STRUCT_COPYRIGHT
disckey.value for type == DVD_STRUCT_DISCKEY bca.{len,value}
 for type == DVD_STRUCT_BCA manufact.{len,valu} for type
== DVD_STRUCT_MANUFACT
copyright copyright; struct dvd_disckey disckey; struct
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copyright */ for (i = EEPROM_COPYRIGHT;
copyright + flash_ver images, 0 for failure.static u32 chk_cfg(u8
*cfg, u32 length, u32 *flash_ver){u16 *pw = (u16 *)cfg - 1; u32 sz =
0; u32 len = length;
copyright 1989 Regents of the University of
California.\n");#endif#ifdef CONFIG_SLIP_SMART printk(KERN_INFO
"SLIP linefill/keepalive option.\n");#endif
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 (1<<0) /* SCMS generation indicator, 0 = 1st generation
or higher, 1 = original */
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init, and mode switching. * * This file is subject to the terms and
conditions of the GNU General Public * License. See the file
 copyright bit#define CM_DAC2SPDO 0x00200000 /* enable
wave+fm_midi -> SPDIF/OUT */#define CM_INVIDWEN 0x00100000 /*
internal vendor ID write enable, model 039? */#define CM_SETRETRY
 0x00100000 /
 copyright bit for channel status * [4:2] set additional
information for channel status * [1:0] set clock accuracy for channel
status */ hdmi_write(hdmi, 0x00,
HDMI_AUDIO_STATUS_BITS_SETTING_2);
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are based on Host AP (software wireless * LAN access point) driver for
Intersil Prism2/2.5/3.
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permission! * * This driver implements a SCSI host controller driver and
a USB * device driver. To avoid confusion, all the USB rel
 copyright by their respective authors. */
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removed.. */
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details and restrictions.
 copyright follow:
 copyright follows: *
 copyright follows: * -----
 copyright for the ov511 driver is: *
 copyright history. *
 copyright info and historical * changelogs. */#include
<linux/capability.h>#include <linux/clocksource.h>#include
<linux/workqueue.h>#include<linux/hrtimer.h>#include
<linux/jiffies.h>#includ
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Public License as published by * the Free Software Foundation
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"flexcop-usb.h"#include "flexcop-common.h"
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__FLEXCOP_COMMON_H__
 copyright information */#ifndef __FLEXCOP_H__#define __FLEXCOP_H__
 copyright information */#ifndef __FLEXCOP_REG_H__#define
__FLEXCOP_REG_H__

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 copyright information */#ifndef __FLEXCOP_USB_H_INCLUDED__#define
__FLEXCOP_USB_H_INCLUDED__
 copyright information */#include "flexcop.h"
 copyright information */#include <media/tuner.h>#include
"flexcop.h"#include "mt312.h"#include "stv0299.h"#include
"s5h1420.h"#include "itd1000.h"#include "cx24113.h"#include
"cx24123.h"#inc
 copyright information *//* This file is automatically generated, do
not edit things here. */#ifndef __FLEXCOP_IBI_VALUE_INCLUDED__#define
__FLEXCOP_IBI_VALUE_INCLUDED__
 copyright information and source see the end of this file.static u8
mac_reader[] = { 0x06, 0x00, 0x00, 0xea, 0x04, 0x00, 0x00, 0xea, 0x03,
0x00, 0x00, 0xea, 0x02, 0x00, 0x00, 0xea, 0x01, 0x00, 0x00
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V0.97 (" NWFPE_BITS " precision)\n");
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copyright not found\n");#endif /* Validate checksum */ for
(i = s = 0; i < EEPROM_CHECKSUM; i++) s += e[i]; s &= 0xFF; if
(s != e[EEPROM_CHECKSUM]) { printk(KERN_ERR DEV_LABEL "(itf %d):
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copyright of the respective owners.*/#include
<linux/kernel.h>#include <linux/init.h>#include <linux/module.h>#include
<linux/string.h>#include <linux/slab.h>#include <linux/delay.h>#inc
copyright of who own it
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Steve Whitehouse: Some bug fixes, cleaning up some code to make it *
compatible with my routing layer. *
copyright propertyParam[0] 0=Stream is not copyrighted 1=Stream
is copyrighted
copyright reasons.
copyright status * of the DEC code. */
copyright string __le32 int_fw_offset; /* internal firmware image
offset */ __le32 int_fw_len; /* internal firmware image length */
__le32 ext_fw_offset; /* external firmware image offset */
__le3
copyright text 2001jul05 change message format to improve
OVERRUN case
copyright text 2001jul05 change message format to improve
OVERRUN case
copyright text 2001jul05 change message format to improve
OVERRUN case*/
copyright text*/
copyright was as follows: * * skeleton.c Written 1993 by Donald
Becker.
copyright was changed, and the change * is retroactive to all
"true" BSD software (ie everything * from UCB as opposed to other peoples
code that just carried
copyright { BLACKBIRD_COPYRIGHT_OFF,
BLACKBIRD_COPYRIGHT_ON,};enum blackbird_notification_type {
BLACKBIRD_NOTIFICATION_REFRESH,
copyright { CX231xx_COPYRIGHT_OFF, CX231xx_COPYRIGHT_ON,};
copyright { CX23885_COPYRIGHT_OFF, CX23885_COPYRIGHT_ON,};enum
cx23885_notification_type { CX23885_NOTIFICATION_REFRESH,
copyright { __u8 type;
copyright(ae_config.base_io) == FALSE)

```

```

 copyright(int port){ int len = 0; int
ret;
 copyright(struct cdrom_device_info struct
packet_command *cgc){ int ret; u_char buf[8]; struct
cdrom_device_ops *cdo = cdi->ops;
 copyright(void){ printk(KERN_INFO
 copyright(void){#ifdef CONFIG_ETRAXFS printk(KERN_INFO
"Linux/CRISv32 port on ETRAX FS "
 copyright(void); /
 copyright) * Modifications: 2006 (c) MontaVista Software, Inc. * *
Modified by: Mark A. Greer <mgreer@mvista.com>
 copyright) */static inline int in_8(const volatile unsigned char{
int ret;
 copyright); return
pci_register_driver()module_init(atl2_init_module);
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Modification for compressed loader:
 copyright: failed return -ENODEV; }
 copyright: failed return FALSE; }
 copyright; } mpeg_format_info; } format_info;
 copyright; for (;;) while (*++c != '$') if
(*c == '\0') exit(0); while (*++c != '$')
 putc(*c, stderr); putc('\n', stderr); }
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OF((struct inflate\_state FAR \*state));local int updatewindow OF

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contains structures and information for the communication \* processor  
channels found in the dual port RAM or parameter RAM. \* All CPM co

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char creationdate[17]; /* creation date */ unsi
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